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FEE TRANSMITTAL
FOR FY 2001

APR 30 2001

Patent fees are subject to annual revision.

TOTAL AMOUNT OF PAYMENT

(\$ 290.00)

METHOD OF PAYMENT

The Commissioner is hereby authorized to charge indicated fees and credit any overpayments to:

Deposit Account Number

19-2380

Deposit Account Name

Nixon Peabody LLP

Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17

Applicant claims small entity status. See 37 CFR 1.27

2. Payment Enclosed:

Check Credit Card Money Order Other

FEE CALCULATION

1. BASIC FILING FEE

Large Entity Fee Code	Small Entity Fee Code	Fee Description	Fee Paid
101	710	201 355	Utility filing fee
106	320	206 160	Design filing fee
107	490	207 245	Plant filing fee
108	710	208 355	Reissue filing fee
114	150	214 75	Provisional filing fee

SUBTOTAL (1) (\$ 0)

2. EXTRA CLAIM FEES

Total Claims	Independent Claims	Fee from below	Fee Paid
72	10	0 X 0 = 0	0
Multiple Dependent		0 = 0	0

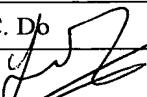
Large Entity Fee Code	Small Entity Fee Code	Fee Description	Fee Paid
103	18	203 9	Claims in excess of 20
102	80	202 40	Independent claims in excess of 3
104	270	204 135	Multiple dependent claim, if not paid
109	80	209 40	** Reissue independent claims over original patent
110	18	210 9	** Reissue claims in excess of 20 and over original patent

SUBTOTAL (2) (\$ 0)

** or number previously paid, if greater; For Reissues, see above

SUBMITTED BY

Complete (if applicable)

Name (Print/Type)	Luan C. Do	Registration No. (Attorney/Agent)	38,434	Telephone	703-790-9110
Signature				Date	April 26, 2001

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NVA179535.1



40/Tamura
5-8-01

BOX AFTER FINAL
PATENT
740756-001400

UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:) Art Unit: 2815
Shunpei YAMAZAKI et al.)
Application No.: 08/520,079)
Filed: August 28, 1995)
For: SEMICONDUCTOR CIRCUIT)
FOR ELECTRO-OPTICAL DEVICE)
AND METHOD OF)
MANUFACTURING THE SAME)

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on 4/26/01
Shunpei YAMAZAKI

AMENDMENT

Commissioner of Patents
Washington, D.C. 20231

April 26, 2001

Dear Sir:

In response to the Examiner's Final Office Action mailed December 26, 2000, the period for responding having been extended one (1) month, please consider the following amendments and remarks in connection with the above-identified application.

IN THE CLAIMS:

Please amend claims 73, 80, 87, 93, 99, 105, 111, 117, 123 and 129 as follows:

73. (Thrice Amended) A thin film transistor comprising:
a crystalline semiconductor island over a substrate having an insulating surface;
source and drain regions in said semiconductor island;
a channel forming region between said source and drain regions;
a gate insulating film adjacent to at least said channel forming region;
a gate electrode adjacent to said channel forming region having said gate insulating film
therebetween,